Application Number:	100	10604491						
Filing Date:	25-	25-Jul-2003						
Title of Invention:	Mu	Multiprocessor subsystem in SoC with bridge between processor clusters interconnection and SoC system bus						
First Named Inventor:	Ch	Christos J. Georgiou						
Filer:	Ste	Steven Fischman/Grace Colucci						
Attorney Docket Number:	FIS	FIS920030113						
Filed as Large Entity								
Utility Filing Fees								
Description		Fee Code	Quantity	Amount	Sub-Total ir USD(\$)			
Basic Filing:								
Pages:								
Claims:								
Miscellaneous-Filing:								
Petition:								
Patent-Appeals-and-Interference:								
Post-Allowance-and-Post-Issuance:								

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	790	790
	Tota	790		